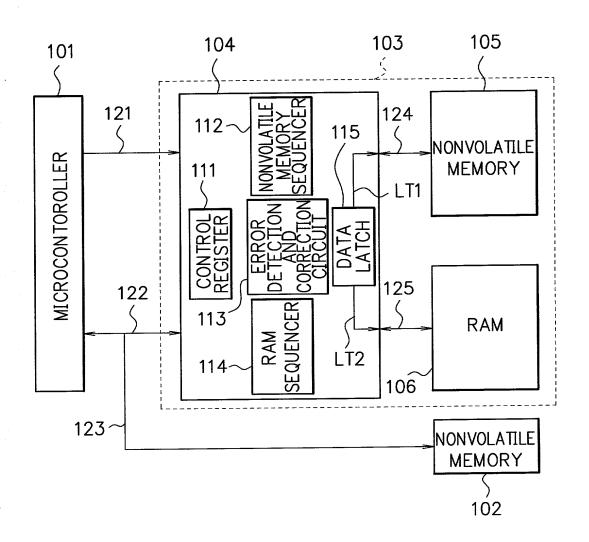
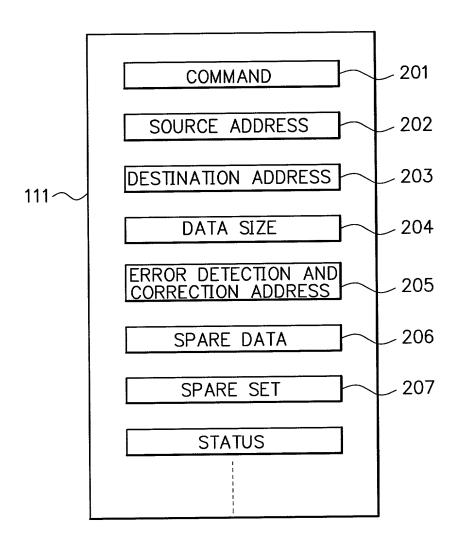
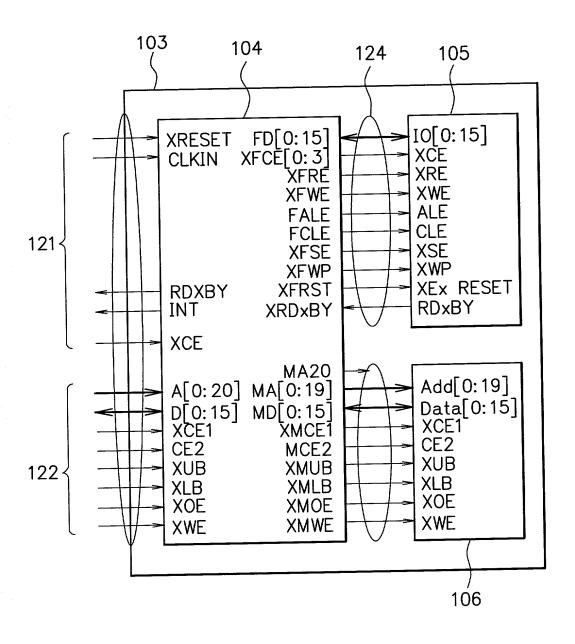
F I G. 1

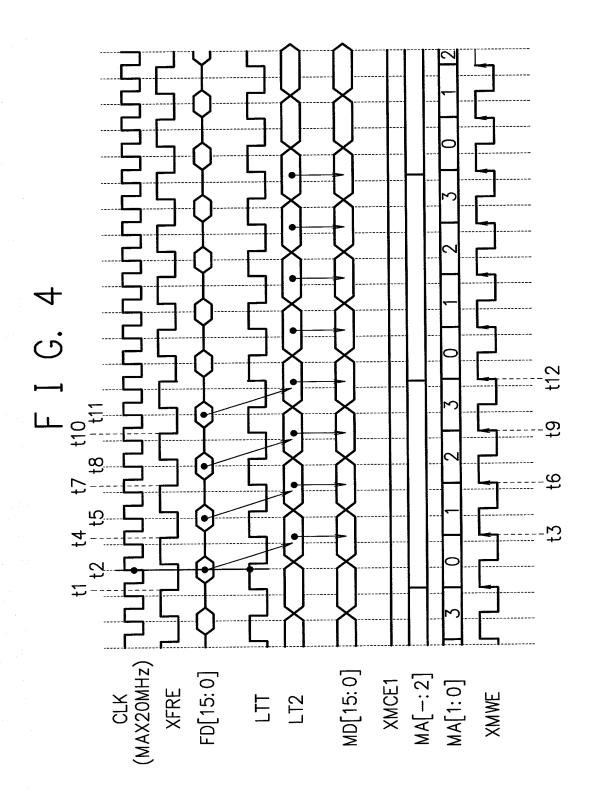


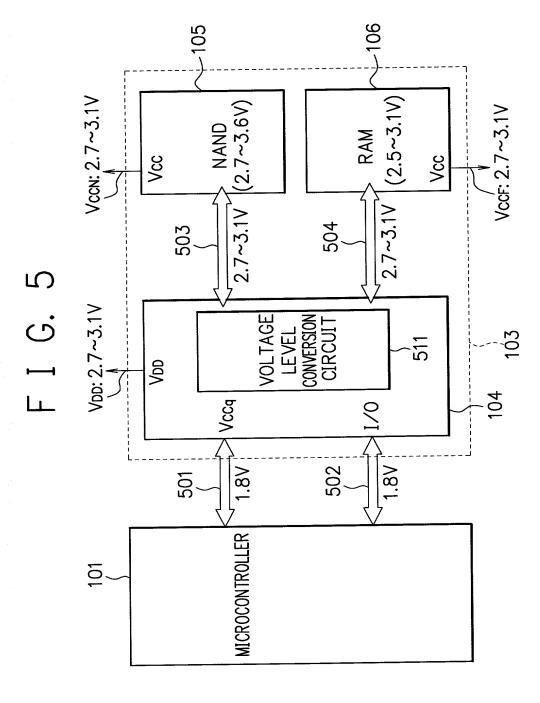
F I G. 2

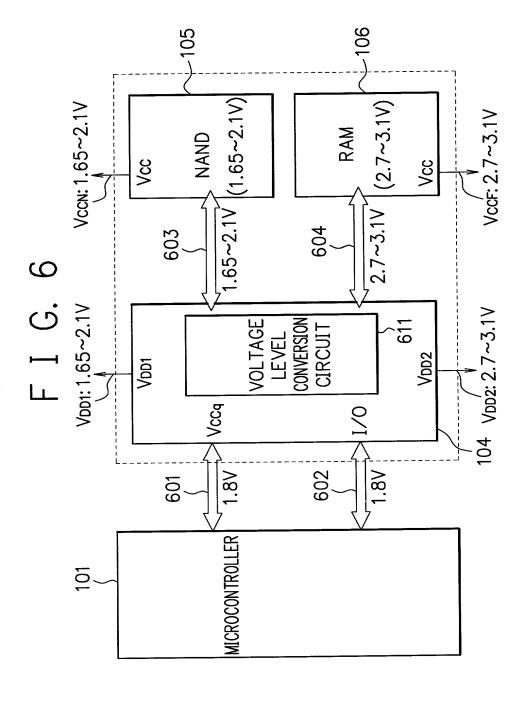


F I G. 3







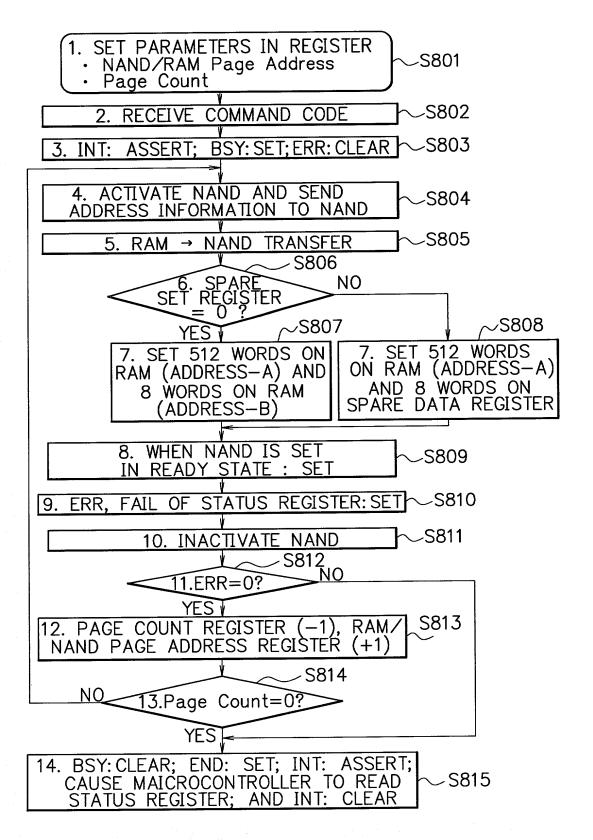


712a MICROCONTROLLER 741 ACTUAL ACTUAL ACTUAL
/> DATA DATADATA (712b ACTUAL DATA AREA + SPARE DATA AREA (2) ACTUAL DATA AREA + SPARE DATA AREA (1) ACTUAL DATA AREA + SPARE DATA AREA (3) <STREAM DATA> (1) 703~

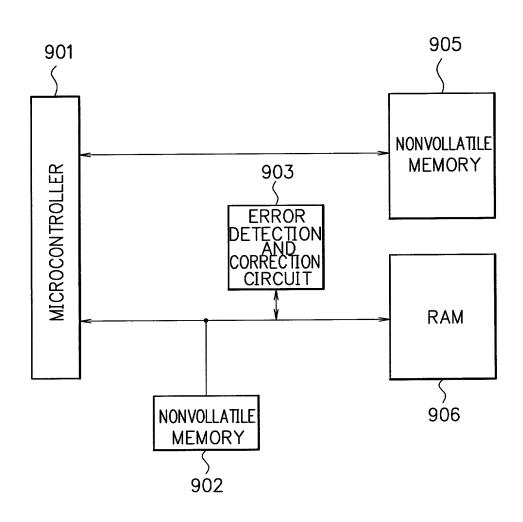
~712

713b 713c

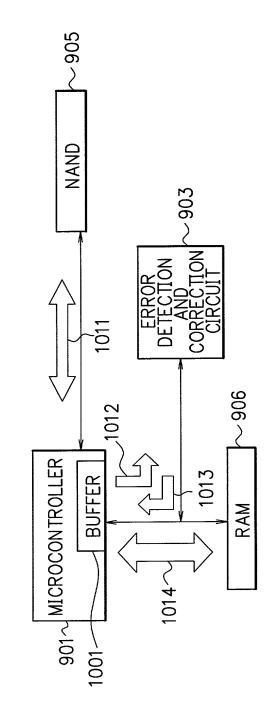
F I G. 8

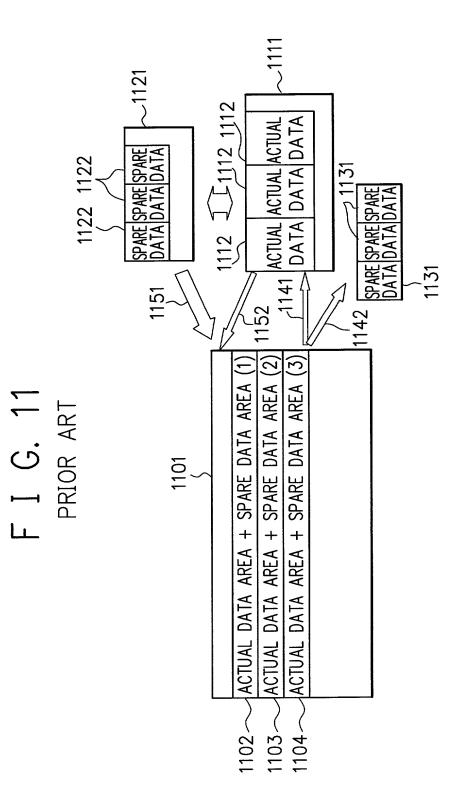


F I G. 9 PRIOR ART

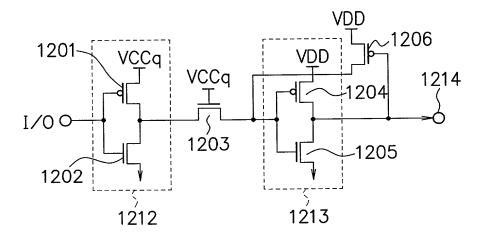


F I G. 10 PRIOR ART





F I G. 12



F I G. 13

